



235	654	Subclass	ISSUE CLASSIFICATION
		Class	

PATENT NUMBER.

U.S. UTILITY Patent Application

O.I.P.E. 375

PATENT DATE

SCANNED

Q.A.

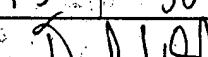
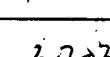
APPLICATION NO.	CONT/PRIOR.	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/523990		235	454	2876	WALSH, JR.

APPLICANT'S
Name: SHIUNG Lin
Chien Lee

TYPE: Method of manufacture and identification of semiconductor chip
marked for identification with internal marking indicia and
protection thereof by non-black layer and device produced thereby

PTO-2040
12/99

ISSUING CLASSIFICATION

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print.Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	15	38	1	23	1
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	 (Primary Examiner)			3303  (Date)	
				NOTICE OF ALLOWANCE MAILED	
				 (Date)	
				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	 (Legal Instruments Examiner)			 (Date)	
				ISSUE BATCH NUMBER	

E - PTO 4884

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